

$V_{DSS}$	1700V
$R_{DS(on)}$ (Typ.)	750m $\Omega$
$I_D$	6A
$P_D$	57W

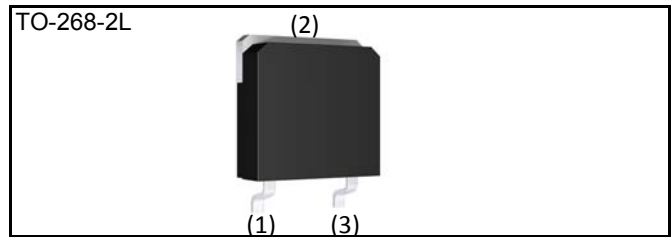
#### ●Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Long creepage distance with no center lead
- 4) Simple to drive
- 5) Pb-free lead plating ; RoHS compliant

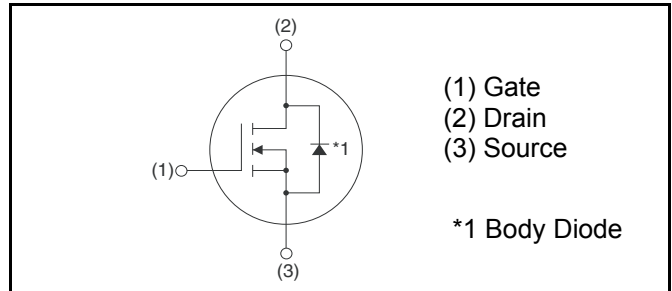
#### ●Application

- Auxiliary power supplies
- Switch mode power supplies

#### ●Outline



#### ●Inner circuit



#### ●Packaging specifications

Type	Packing	Embossed tape
	Reel size (mm)	330
	Tape width (mm)	24
	Basic ordering unit (pcs)	400
	Taping code	TB
	Marking	SCT2750NY

#### ●Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	1700	V	
Continuous drain current	$T_c = 25^\circ\text{C}$	$I_D^{*1}$	5.9	A
	$T_c = 100^\circ\text{C}$	$I_D^{*1}$	4	A
Pulsed drain current	$I_{D,pulse}^{*2}$	14	A	
Gate - Source voltage (DC)	$V_{GSS}$	-6 to 22	V	
Gate - Source surge voltage ( $t_{surge} < 300\text{nsec}$ )	$V_{GSS,surge}^{*3}$	-10 to 26	V	
Power dissipation ( $T_c = 25^\circ\text{C}$ )	$P_D$	57	W	
Junction temperature	$T_j$	175	$^\circ\text{C}$	
Range of storage temperature	$T_{stg}$	-55 to +175	$^\circ\text{C}$	

### ● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	$R_{thJC}$	-	2.04	2.65	°C/W

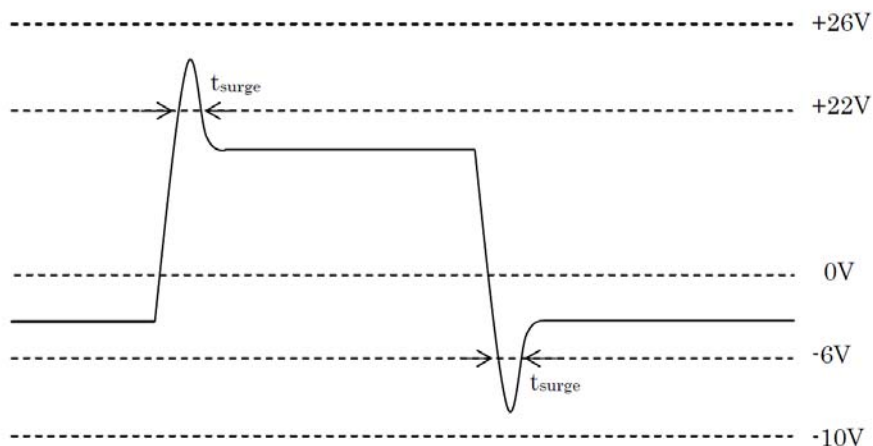
### ● Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$	1700	-	-	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 1700\text{V}, V_{GS} = 0\text{V}$ $T_j = 25^\circ\text{C}$	-	0.1	10	$\mu\text{A}$
		$T_j = 150^\circ\text{C}$	-	0.2	-	
Gate - Source leakage current	$I_{GSS+}$	$V_{GS} = +22\text{V}, V_{DS} = 0\text{V}$	-	-	100	nA
Gate - Source leakage current	$I_{GSS-}$	$V_{GS} = -6\text{V}, V_{DS} = 0\text{V}$	-	-	-100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 0.63\text{mA}$	1.6	2.8	4.0	V

\*1 Limited only by maximum temperature allowed.

\*2  $PW \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$

\*3 Example of acceptable  $V_{GS}$  waveform



\*4 Pulsed

**●Electrical characteristics (T<sub>a</sub> = 25°C)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Static drain - source on - state resistance	R <sub>DS(on)</sub> <sup>*4</sup>	V <sub>GS</sub> = 18V, I <sub>D</sub> = 1.7A T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	- -	750 1088	975 -	mΩ
Gate input resistance	R <sub>G</sub>	f = 1MHz, open drain	-	49	-	Ω
Transconductance	g <sub>fs</sub> <sup>*4</sup>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1.7A	-	0.6	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	275	-	pF
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 800V	-	19	-	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	7	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	V <sub>GS</sub> = 0V V <sub>DS</sub> = 0V to 800V	-	21	-	pF
Turn - on delay time	t <sub>d(on)</sub> <sup>*4</sup>	V <sub>DD</sub> = 500V, I <sub>D</sub> = 1.7A	-	19	-	ns
Rise time	t <sub>r</sub> <sup>*4</sup>	V <sub>GS</sub> = 18V/0V	-	24	-	
Turn - off delay time	t <sub>d(off)</sub> <sup>*4</sup>	R <sub>L</sub> = 294Ω	-	41	-	
Fall time	t <sub>f</sub> <sup>*4</sup>	R <sub>G</sub> = 0Ω	-	63	-	
Turn - on switching loss	E <sub>on</sub> <sup>*4</sup>	V <sub>DD</sub> = 800V, I <sub>D</sub> = 1.7A V <sub>GS</sub> = 18V/0V R <sub>G</sub> = 0Ω, L = 2mH	-	76	-	μJ
Turn - off switching loss	E <sub>off</sub> <sup>*4</sup>	*E <sub>on</sub> includes diode reverse recovery	-	33	-	

**●Gate Charge characteristics (T<sub>a</sub> = 25°C)**

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q <sub>g</sub> <sup>*4</sup>	V <sub>DD</sub> = 500V	-	17	-	nC
Gate - Source charge	Q <sub>gs</sub> <sup>*4</sup>	I <sub>D</sub> = 1.5A	-	5	-	
Gate - Drain charge	Q <sub>gd</sub> <sup>*4</sup>	V <sub>GS</sub> = 18V	-	6.5	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> = 500V, I <sub>D</sub> = 1.5A	-	11.0	-	V

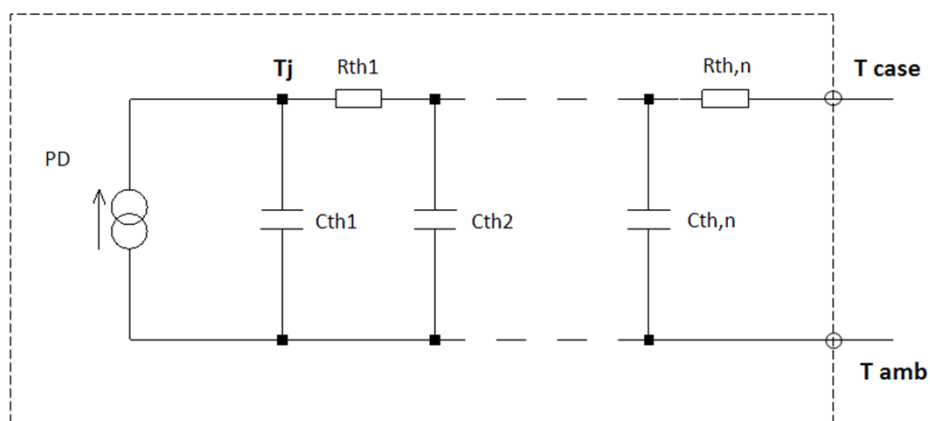
●Body diode electrical characteristics (Source-Drain) ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Inverse diode continuous, forward current	$I_S^{*1}$	$T_C = 25^\circ\text{C}$	-	-	5.9	A
Inverse diode direct current, pulsed	$I_{SM}^{*2}$		-	-	14	A
Forward voltage	$V_{SD}^{*4}$	$V_{GS} = 0\text{V}, I_S = 1.7\text{A}$	-	4.3	-	V
Reverse recovery time	$t_{rr}^{*4}$	$I_F = 1.7\text{A}, V_R = 800\text{V}$ $di/dt = 290\text{A}/\mu\text{s}$	-	26	-	ns
Reverse recovery charge	$Q_{rr}^{*4}$		-	18	-	nC
Peak reverse recovery current	$I_{rrm}^{*4}$		-	1.3	-	A

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
$R_{th1}$	243m	K/W
$R_{th2}$	1529m	
$R_{th3}$	268m	

Symbol	Value	Unit
$C_{th1}$	352 $\mu$	Ws/K
$C_{th2}$	1.57m	
$C_{th3}$	68.7m	



●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

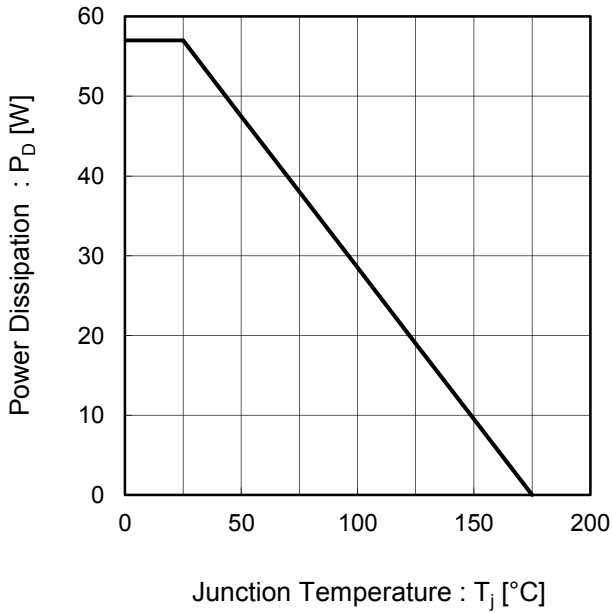


Fig.2 Maximum Safe Operating Area

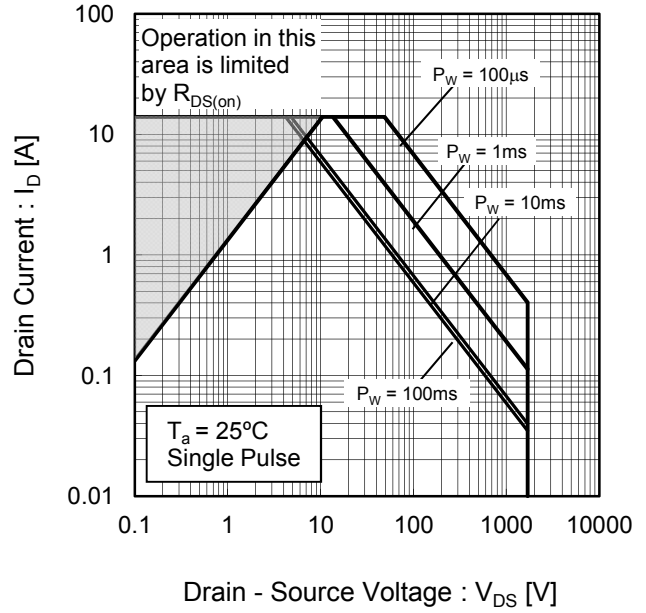
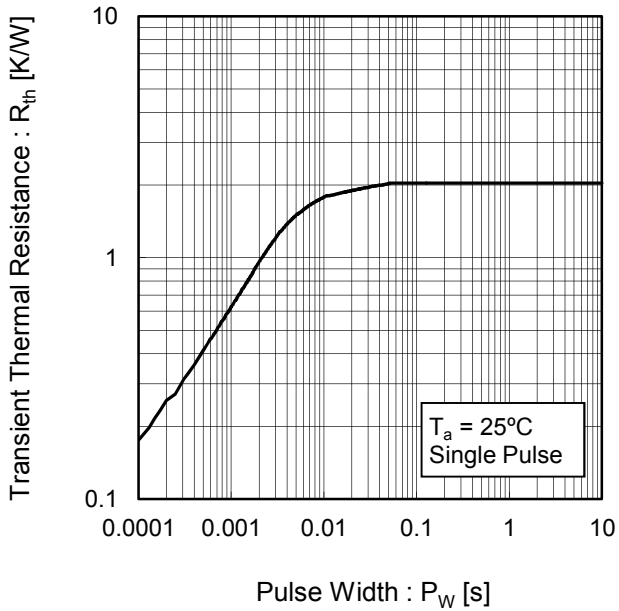


Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



●Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)

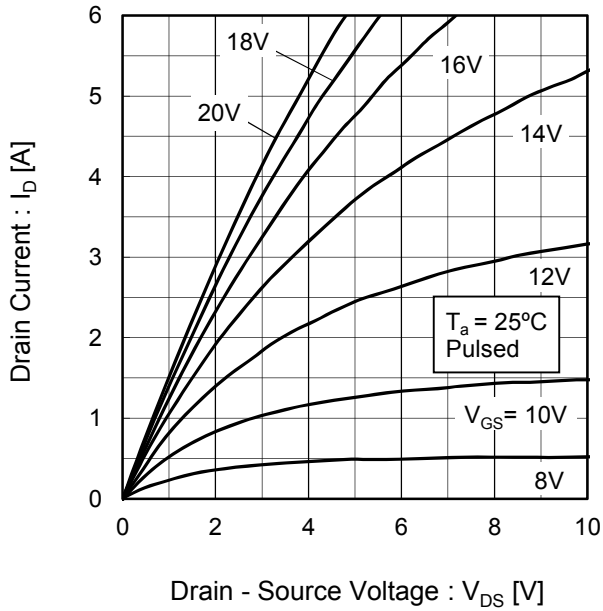


Fig.5 Typical Output Characteristics(II)

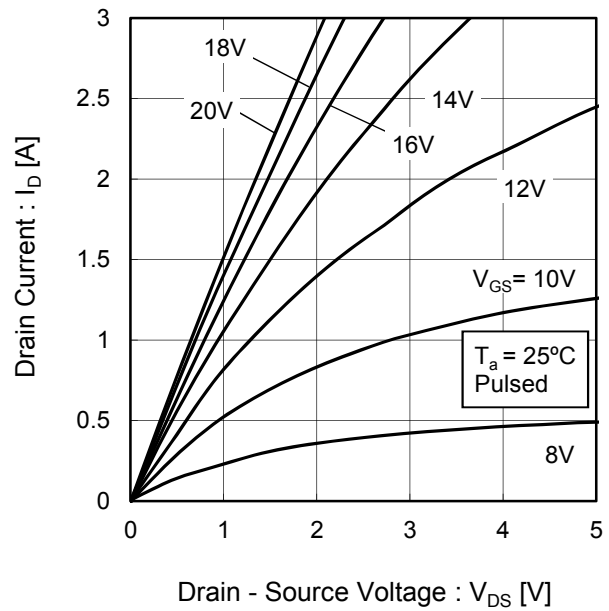


Fig.6  $T_j = 150^\circ\text{C}$  Typical Output Characteristics(I)

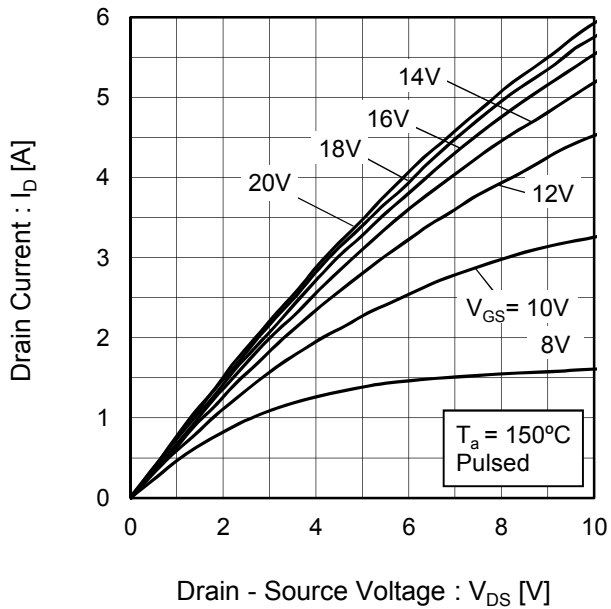
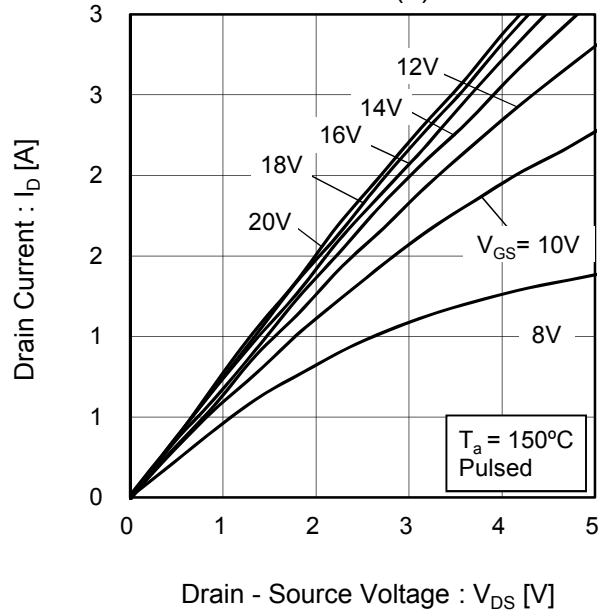


Fig.7  $T_j = 150^\circ\text{C}$  Typical Output Characteristics(II)



●Electrical characteristic curves

Fig.8 Typical Transfer Characteristics (I)

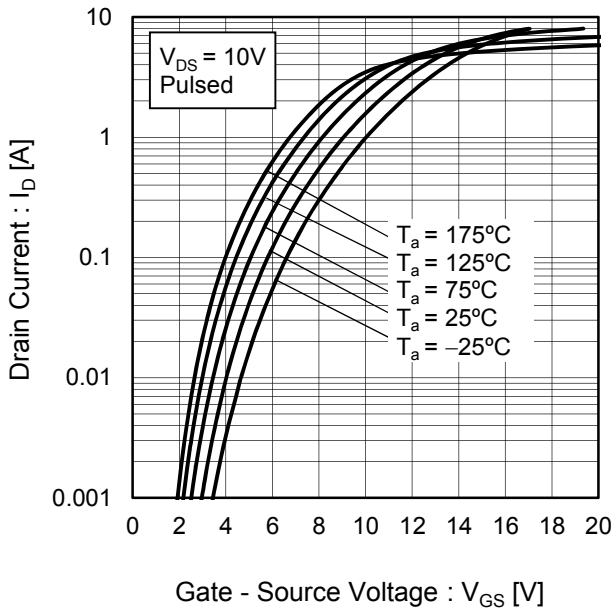


Fig.9 Typical Transfer Characteristics (II)

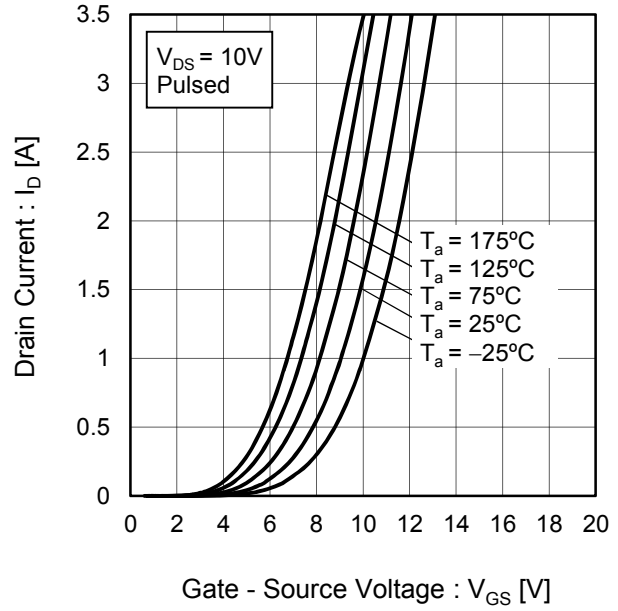


Fig.10 Gate Threshold Voltage vs. Junction Temperature

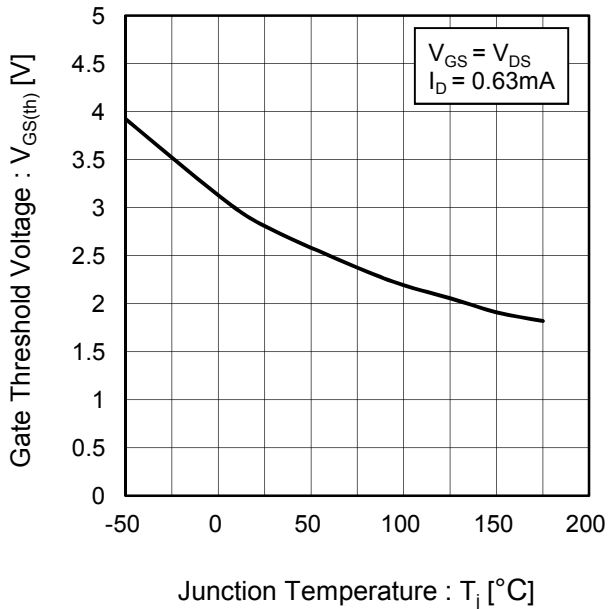
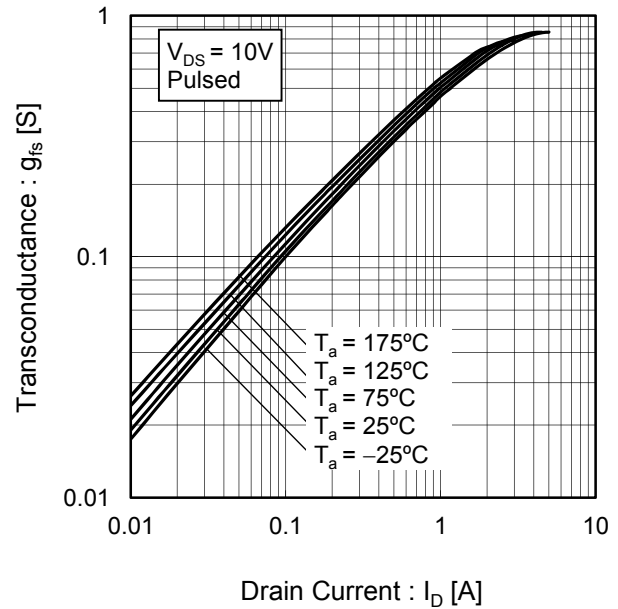


Fig.11 Transconductance vs. Drain Current



●Electrical characteristic curves

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

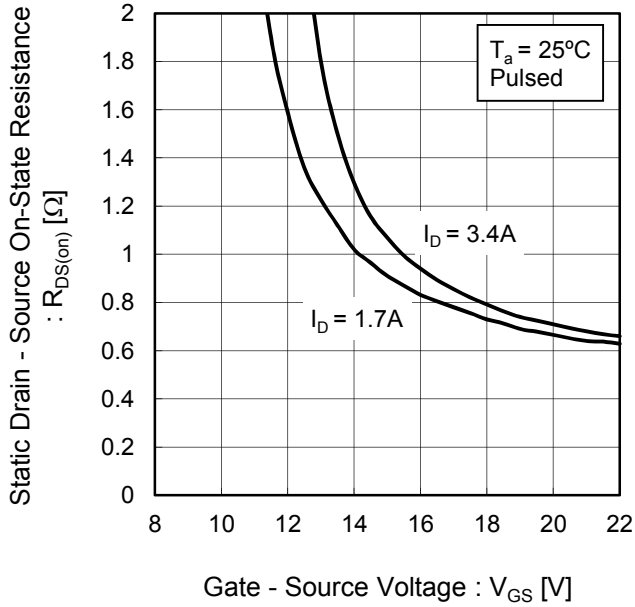


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

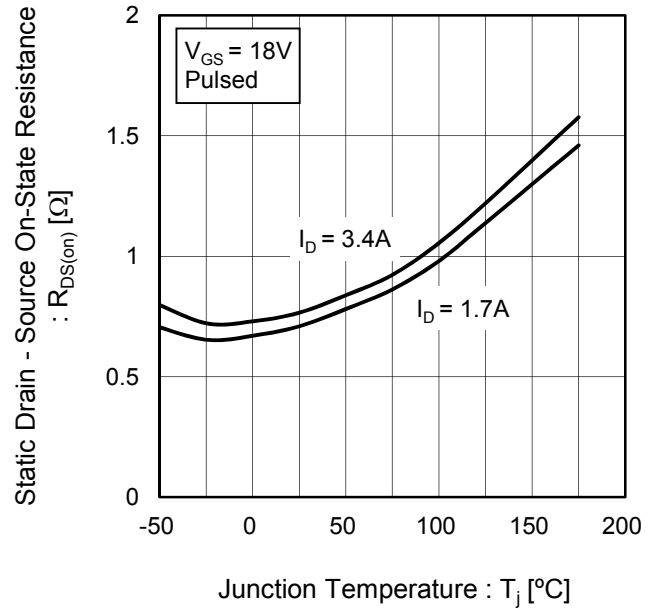
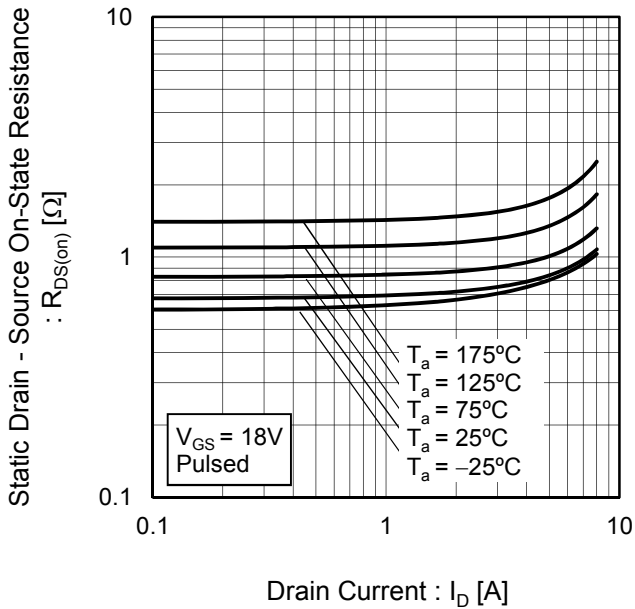


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current





●Electrical characteristic curves

Fig.15 Typical Capacitance vs. Drain - Source Voltage

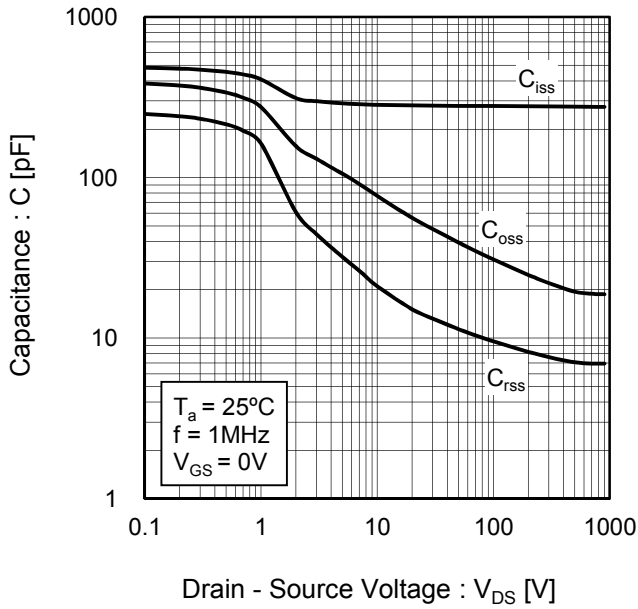


Fig.16 Coss Stored Energy

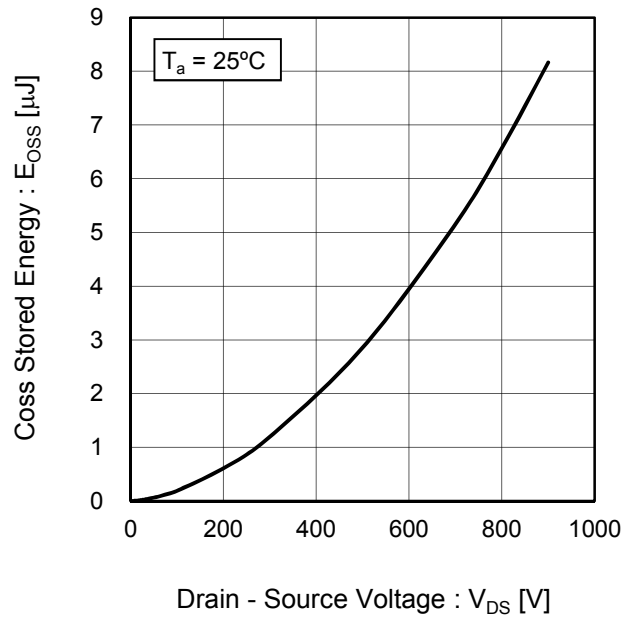


Fig.17 Switching Characteristics

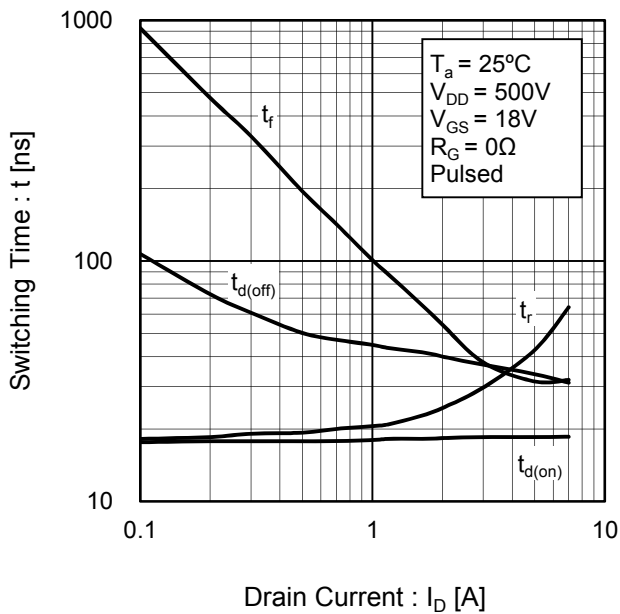
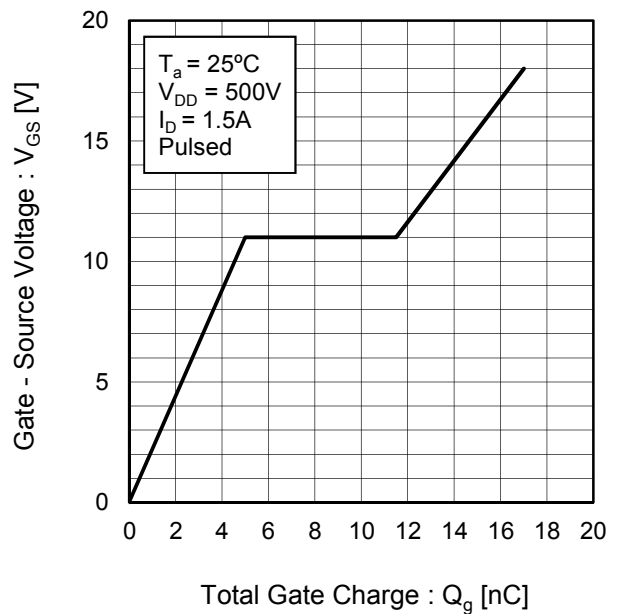


Fig.18 Dynamic Input Characteristics



●Electrical characteristic curves

Fig.19 Typical Switching Loss vs. Drain - Source Voltage

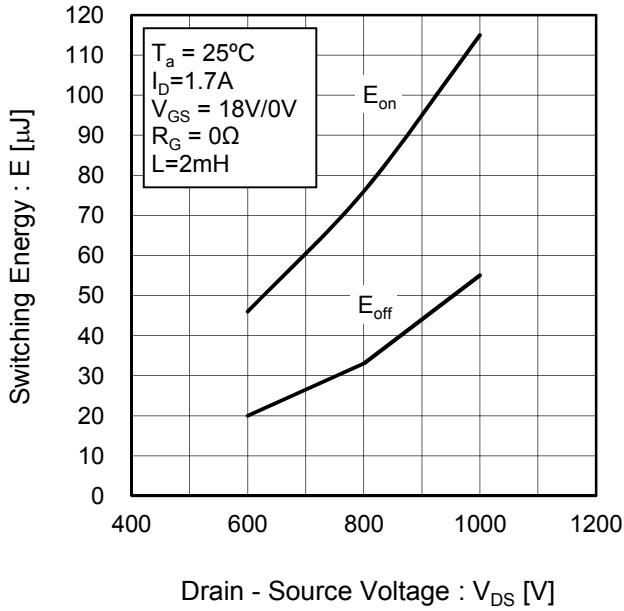


Fig.20 Typical Switching Loss vs. Drain Current

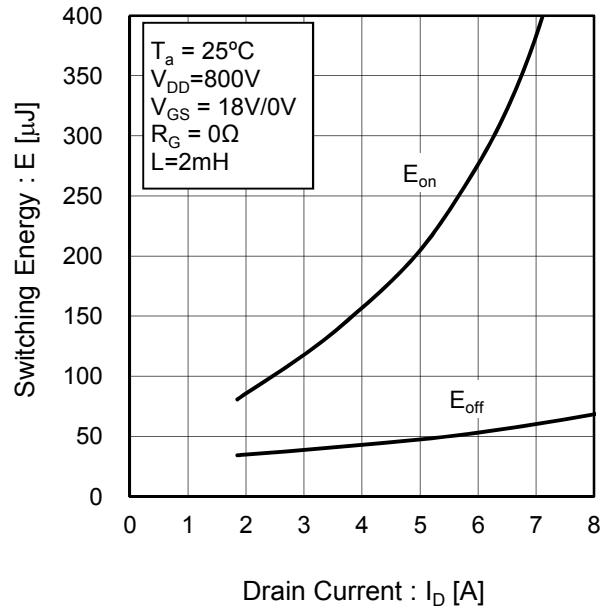
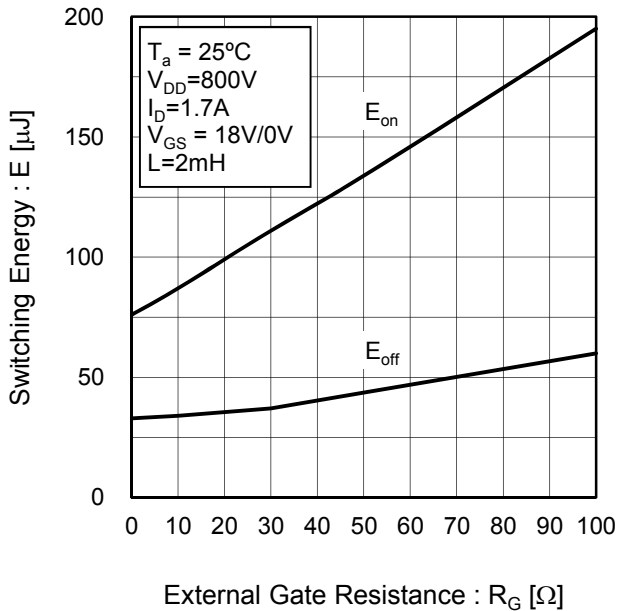


Fig.21 Typical Switching Loss vs. External Gate Resistance



●Electrical characteristic curves

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage

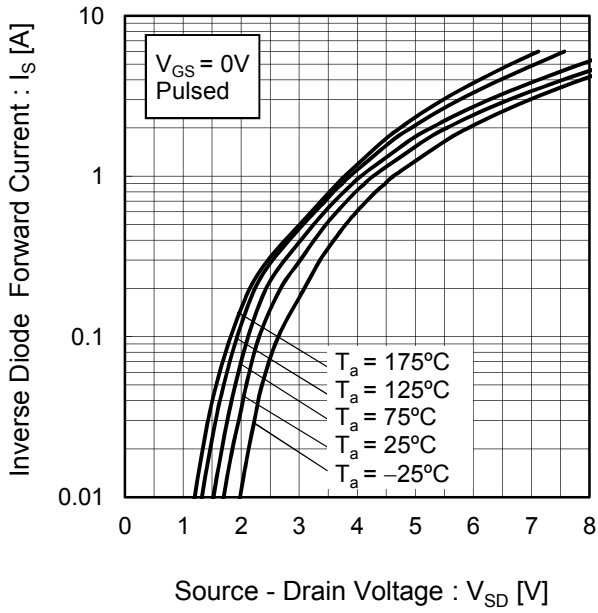
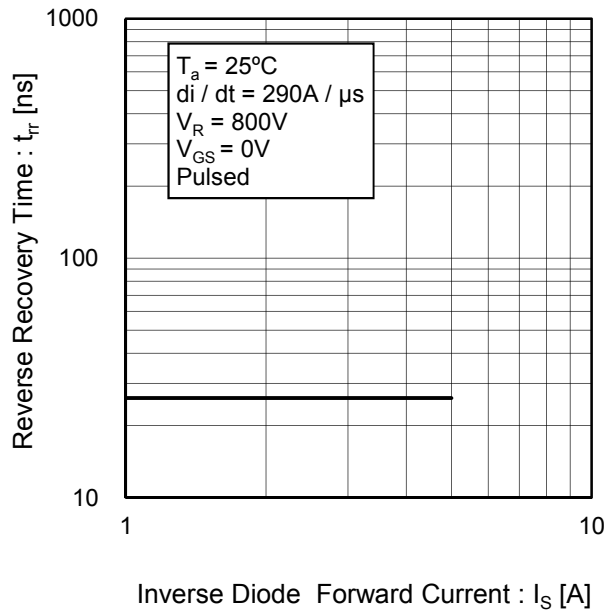


Fig.23 Reverse Recovery Time vs. Inverse Diode Forward Current



●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit



Fig.1-2 Switching Waveforms



Fig.2-1 Gate Charge Measurement Circuit



Fig.2-2 Gate Charge Waveform

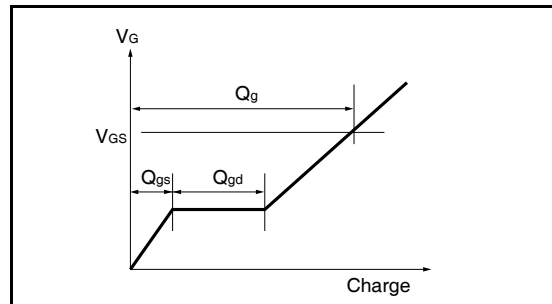


Fig.3-1 Switching Energy Measurement Circuit

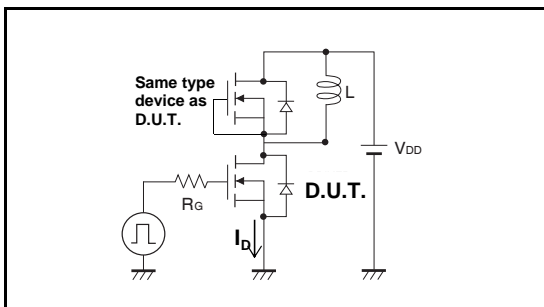


Fig.3-2 Switching Waveforms

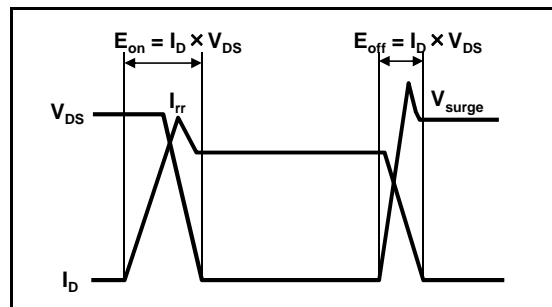


Fig.4-1 Reverse Recovery Time Measurement Circuit

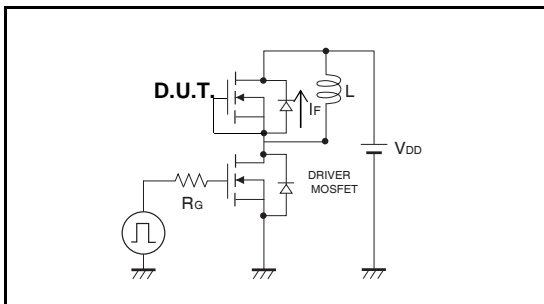
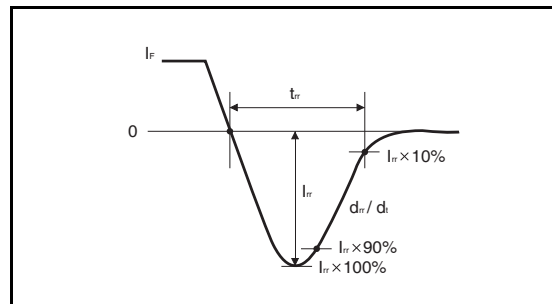


Fig.4-2 Reverse Recovery Waveform



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