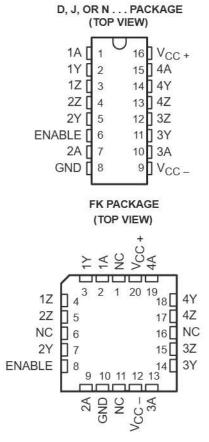
- Similar to a Dual Version of SN55/75110A Line Driver
- Improved Stability Over Supply Voltage and Temperature Ranges
- Constant-Current Outputs
- High Output Impedance
- High Common-Mode Output Voltage Range
   3 V to 10 V
- Glitch-Free Power-Up/Power-Down Operation
- TTL-Input Compatibility
- Common-Enable Circuit
- Designed to Be Interchangeable With Motorola MC3453 and Military-Temperature-Range Version of MC3553

#### description

The MC3453 and MC3553 feature four line drivers with a common-enable input. When the ENABLE input is high, a constant output current is switched between each pair of output terminals in response to the logic level at that channel input. When the ENABLE is low, all channel outputs are nonconductive (transistors biased to cutoff). This minimizes loading in party-line systems where a large number of drivers share the same line.

The driver outputs have a common-mode voltage range of -3 V to 10 V, allowing common-mode voltages on the line without affecting driver performance.

All outputs should be maintained within the recommended common-mode output voltage range to ensure that the channels do not interact with each other. To minimize power dissipation, all unused outputs should be grounded.



NC-No internal connection

#### FUNCTION TABLE

LOGIC INPUT	ENABLE INPUT	120000	TPUT RENT Y
Н	Н	On	Off
L	Н	Off	On
Н	L	Off	Off
L	L	Off	Off

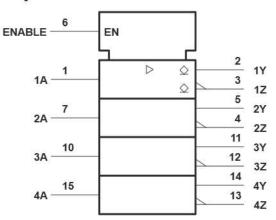
L = low logic level, H = high logic level

All inputs are diode clamped and are designed to satisfy TTL-system requirements. The inputs are tested at 2 V for high-logic-level input conditions and 0.8 V for low-logic-level input conditions. These tests ensure 400 mV of noise margin when interfaced with Series 54/74 TTL.

The MC3453 is characterized for operation from 0°C to 70°C. The MC3553 is characterized for operation over the full military temperature range of –55°C to 125°C.

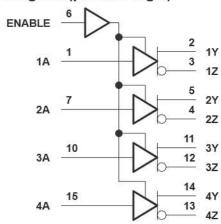
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### logic symbol†

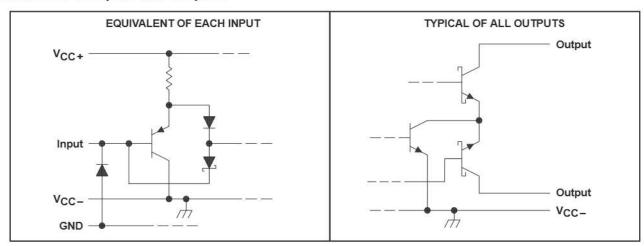


<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

### logic diagram (positive logic)



### schematics of inputs and outputs



### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

7 V	Supply voltage, V <sub>CC+</sub> (see Note 1)
7 V	
5.5 V	Input voltage (any input)
	Output voltage range (any output)
See Dissipation Rating Table	
0°C to 70°C	Operating free-air temperature range: MC3453
	마상이 [19] [18] [18] [18] [18] [18] [18] [18] [18
260°C	
e for 10 seconds: D or N package	
e for 60 seconds: J package	그는 그

NOTE 1: All voltage values are with respect to network ground terminal.



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#### DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{A}} \le 25^{\circ}\mbox{C}$ POWER RATING	OPERATING FACTOR ABOVE T <sub>A</sub> = 25°C	T <sub>A</sub> = 70°C POWER RATING	T <sub>A</sub> = 125°C POWER RATING
D	950 mW	7.6 mW/°C	608 mW	5-6
FK	1375 mW	11.0 mW/°C	880 mW	275 mW
J	1375 mW	11.0 mW/°C	880 mW	275 mW
N	1150 mW	9.2 mW/°C	736 mW	<u></u>

### recommended operating conditions

		MIN	TYP	MAX	UNIT
Cumplifications V	T <sub>A</sub> ≥ 25°C	4.5	5	5.5	V
Supply voltage, V <sub>CC+</sub>	T <sub>A</sub> < 25°C	4.75	5	5.5	V
Cumply valtage V	T <sub>A</sub> ≥ 25°C	-4.5	-5	-5.5	V
Supply voltage, V <sub>CC</sub> -	T <sub>A</sub> < 25°C	-4.75	-5	-5.5	V
High-level input voltage, VIH		2		5.5	V
Low-level input voltage, V <sub>IL</sub>		0		0.8	V
0	Vocr+	0		10	V
Common-mode output voltage range	Vocr-	0		-3	V
On and the state of the state o	MC3453	0		70	°C
Operating free-air temperature, T <sub>A</sub>	MC3553	-55		125	C

## electrical characteristics over recommended operating free-air temperature range, V<sub>CC±</sub> = MAX (unless otherwise noted)

PARAMETER		TEST	TEST CONDITIONST		TYP‡	MAX	UNIT
VIK	Input clamp voltage	I <sub>I</sub> = -12 mA			-0.9	-1.5	V
Ī	On-state output current	V <sub>CC+</sub> = MAX,	V <sub>CC</sub> -= MAX		11	15	m A
IO(on)	On-state output current	$V_{CC}$ + = MIN,	$V_{CC} + = MIN, V_{CC} = MIN$				mA
IO(off)	Off-state output current	V <sub>CC</sub> + = MIN,	V <sub>CC</sub> -= MIN, V <sub>O</sub> = 10 V			100	αA
I <sub>IH</sub> High-level input current		V <sub>I</sub> = 2.4 V			-	40	αA
<sup>I</sup> IH	riigh-ieveriiiput current	$V_I = V_{CC} + max$	$V_I = V_{CC+} max$			1	mA
l <sub>IL</sub>	Low-level input current	V <sub>I</sub> = 0.4 V				-1.6	mA
i	Complete ourself from V-	A inpute at 0.41V	ENABLE at 2 V		33	50	A
ICC+	Supply current from V <sub>CC+</sub>	A inputs at 0.4 V	ENABLE at 0.4 V		33	50	mA
	a	A inpute at 0.437	ENABLE at 2 V		-68	-90	-m- A
ICC-	Supply current from V <sub>CC</sub> -	A inputs at 0.4 V	ENABLE at 0.4 V		-31	-40	mA

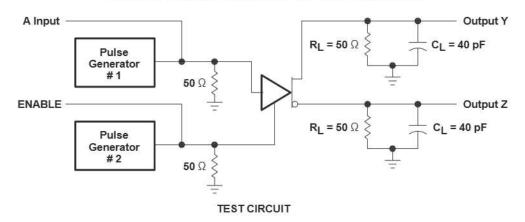
<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at  $V_{CC+} = 5 \text{ V}$ ,  $V_{CC-} = -5 \text{ V}$ , and  $T_A = 25^{\circ}\text{C}$ .

# switching characteristics, V<sub>CC+</sub> = 5 V, V<sub>CC-</sub> = -5 V, R<sub>L</sub> = 50 $\Omega$ , C<sub>L</sub> = 40 pF, T<sub>A</sub> = 25°C

PARAMETER		FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
tPLH	Propagation delay time, low-to-high level output	А	Y or Z			9	15	ns
t <sub>PHL</sub>	Propagation delay time, high-to-low level output	А	Y or Z	Coo Figure 4		7	15	ns
t <sub>PLH</sub>	Propagation delay time, low-to-high level output	ENABLE	Y or Z	See Figure 1		14	25	ns
tPHL	Propagation delay time, high-to-low level output	ENABLE	Y or Z	1		15	25	ns



#### PARAMETER MEASUREMENT INFORMATION



- 3 V A Input 50% 50% 0 V tw1 tw2 3 V **ENABLE** 50% 50% 0 V **tPHL tPLH tPLH** - off Output Y 50% 50% 50% 50% on Output Z 50% 50% tPHL. VOLTAGE WAVEFORMS

NOTES: A. The pulse generators have the following characteristics:  $Z_O$  = 50  $\Omega$ ,  $t_\Gamma$  =  $t_f$  = 10  $\pm$  5 ns,  $t_{W1}$  = 200 ns, PRR  $\leq$  1 MHz,  $t_{W2}$  = 1  $\alpha$ s, PRR  $\leq$  500 kHz.

B. C<sub>L</sub> includes probe and jig capacitance.

Figure 1. Test Circuit and Voltage Waveforms





ti.com 24-Jun-2005

#### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
MC3453D	OBSOLETE	SOIC	D	14	TBD	Call TI	Call TI

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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