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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK1491

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK1491 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

FEATURES

- · Low on-state resistance
 - $R_{DS(on)} = 0.15 \Omega MAX$. (Vgs = 10 V, ID = 13 A)
- Low input capacitance Ciss = 1950 pF TYP.
- · Built-in G-S gate protection diodes
- · High avalanche capability ratings

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	250	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±30	٧
Drain Current (DC)	I _{D(DC)}	±25	Α
Drain Current (pulse) Note1	ID(pulse)	±100	Α
Total Power Dissipation (Tc = 25°C)	PT	120	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note2	las	37.5	Α
Single Avalanche Energy Note2	Eas	1250	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Tch = 25°C, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V

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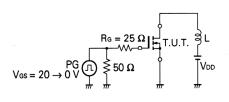
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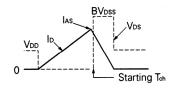
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ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

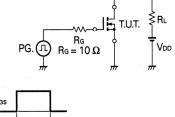
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain to Source On-state Resistance	RDS(on)		0.12	0.15	Ω	Vgs = 10 V, lp = 13 A	
Gate to Source Cutoff Voltage	V _{GS(off)}	2.5		3.5	V	Vps = 10 V, lp = 1 mA	
Forward Transfer Admittance	yfs	7.0		- 1	S	Vps = 10 V, lp = 13 A	
Drain Leakage Current	loss			100	μА	V _{DS} = 250 V, V _{GS} = 0	
Gate to Source Leakage Current	Igss			±10	μΑ	Vgs = ±30 V, Vps = 0	
Input Capacitance	Ciss		1 950		pF	V _{DS} = 10 V V _{GS} = 0 f = 1 MHz	
Output Capacitance	Coss	-	980		pF		
Reverse Transfer Capacitance	Cres		410		pF		
Turn-On Delay Time	td(on)		35		ns	$V_{OD} = 10 \text{ V}$ $V_{DD} = 150 \text{ V}$ $I_D = 13 \text{ A, Rg} = 10 \Omega$ $R_L = 11.5 \Omega$	
Rise Time	tr		110		ns		
Turn-Off Delay Time	td(off)		110		ns		
Fall Time	tr		50		ns		
Total Gate Charge	QG		55		nC	Vgs = 10 V ID = 25 A VDD = 200 V	
Gate to Source Charge	Qgs		12		nC		
Gate to Drain Charge	Qgp		32		nC		
Diode Forward Voltage	V _F (S-D)		1.0		V	IF = 25 A, Vgs = 0	
Reverse Recovery Time	trr		340		ns	IF = 25 A, Vgs = 0	
Reverse Recovery Charge	Qrr		2.3		μC	$di/dt = 50 A/\mu s$	

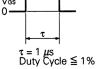
Test Circuit 1: Avalanche Capability

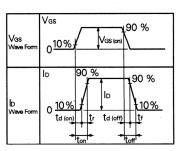




Test Circuit 2: Switching Time



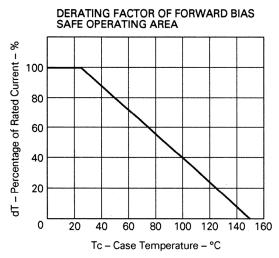


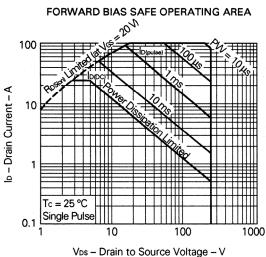


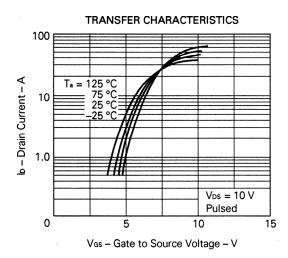
Test Circuit 3: Gate Charge

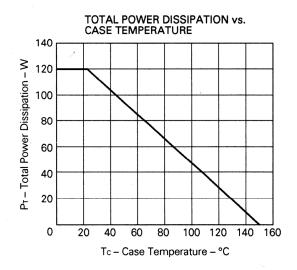
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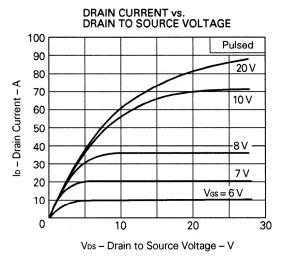
TYPICAL CHARACTERISTICS (Ta = 25 °C)

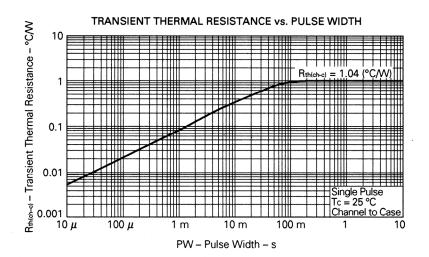




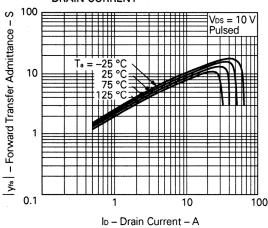


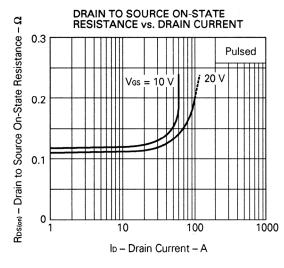


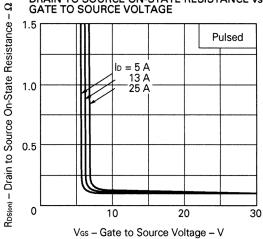




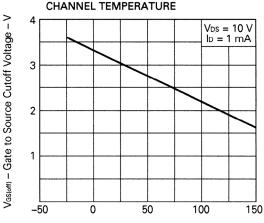




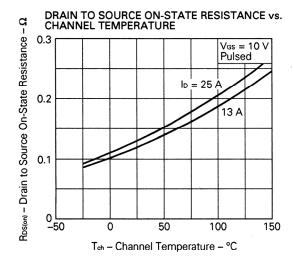


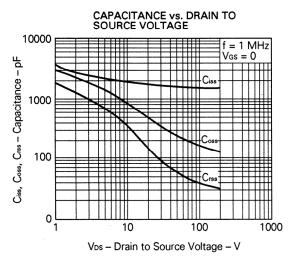


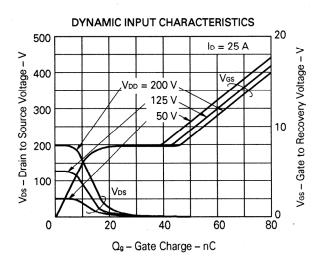
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

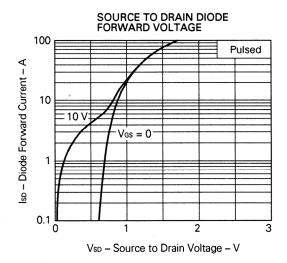


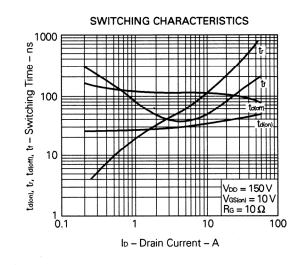
Tch - Channel Temperature - °C

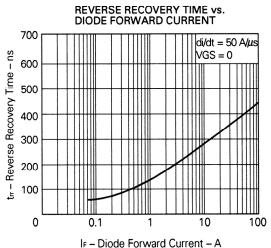


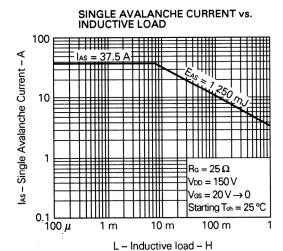


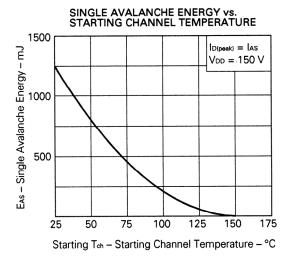






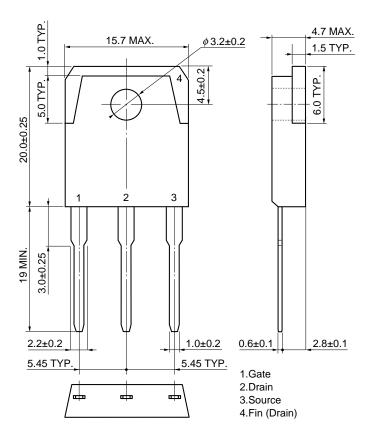




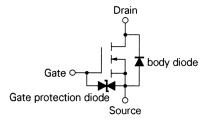


PACKAGE DRAWING (Unit: mm)

<R> TO-3P (MP-88)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

NEC 2SK1491

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