

SANYO Semiconductors DATA SHEET

P-Channel Silicon MOSFET

2SJ571—General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 10V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-400	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		-5	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-20	Α
Allowable Power Dissipation	PD	Tc=25°C	50	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-10mA, VGS=0V	-400			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-320V, V _{GS} =0V			-1.0	mA
Gate-to-Source Leakage Current	IGSS	V _{GS} = ±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	VGS(off)	VDS=-10V, ID=-1mA	-2.0		-3.0	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-2.5A	1.3	2.6		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =-2.5A, V _G S=10V		1.5	2.0	Ω
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		1500		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		240		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		90		pF

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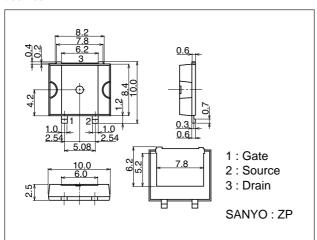
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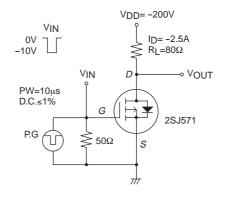
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Office
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		25		ns
Rise Time	t _r	See specified Test Circuit.		70		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		340		ns
Fall Time	tf	See specified Test Circuit.		150		ns
Diode Forward Voltage	V _{SD}	I _S =-5A, V _{GS} =0V		-0.84	-1.5	V

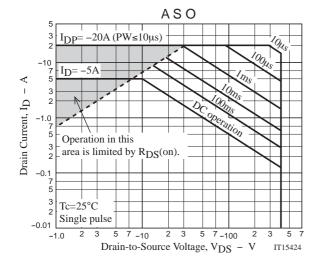
Package Dimensions

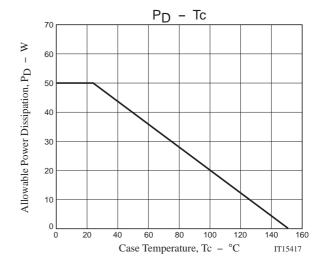
unit : mm (typ) 7002-001



Switching Time Test Circuit







Note on usage: Since the 2SJ571 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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