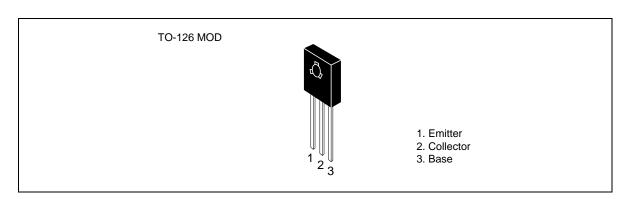
Silicon PNP Epitaxial

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Application

Low frequency high voltage amplifier complementary pair with 2SD668/A

Outline



Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

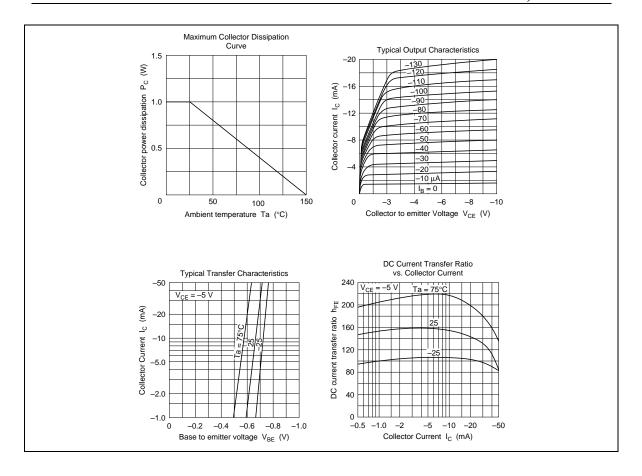
		Ratings		
Item	Symbol	2SB648	2SB648A	Unit
Collector to base voltage	V _{CBO}	-180	-180	V
Collector to emitter voltage	V _{CEO}	-120	-160	V
Emitter to base voltage	V _{EBO}	- 5	- 5	V
Collector current	I _c	- 50	-50	mA
Collector peak current	C(peak)	-100	-100	mA
Collector power dissipation	P _c	1	1	W
Junction temperature	Tj	150	150	°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

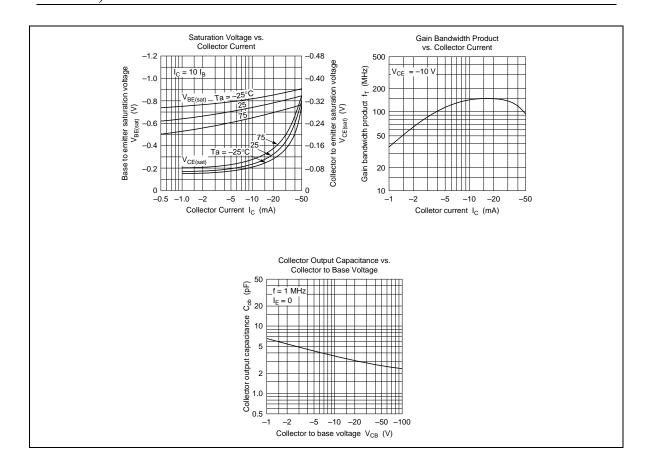
Electrical Characteristics (Ta = 25°C)

		2SB6	48		2SB648A				
Item	Symbol	Min	Тур	Max	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{\text{(BR)CBO}}$	-180	_	_	-180	_	_	V	$I_{c} = -10 \ \mu\text{A}, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{\scriptscriptstyle (BR)CEO}$	-120	_	_	-160	_	_	V	$I_{c} = -1 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{\text{(BR)EBO}}$	- 5	_	_	- 5	_	_	V	$I_{\rm E} = -10 \ \mu \text{A}, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	_	_	-10	_	_	-10	μΑ	$V_{CB} = -160 \text{ V}, I_{E} = 0$
DC current transfer ratio	h _{FE1} *1	60	_	320	60	_	200		$V_{CE} = -5 \text{ V},$ $I_{C} = -10 \text{ mA}$
	h _{FE2}	30	_	_	30	_	_		$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	-2	_	_	-2	V	$I_{c} = -30 \text{ mA},$ $I_{B} = -3 \text{ mA}$
Base to emitter voltage	V_{BE}	_	_	-1.5	_	_	-1.5	V	$V_{ce} = -5 \text{ V},$ $I_c = -10 \text{ mA}$
Gain bandwidth product	$f_{\scriptscriptstyle T}$	_	140	_	_	140	_	MHz	$V_{ce} = -10 \text{ V},$ $I_{c} = -10 \text{ mA}$
Collector output capacitance	Cob		4.5	_		4.5		pF	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1 MHz

Note: 1. The 2SB648 and 2SB648A are grouped by $h_{\rm FE1}$ as follows.

	В	С	D
2SB648	60 to 120	100 to 200	160 to 320
2SB648A	60 to 120	100 to 200	_





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HITACHI

Hitachi, Ltd.
Semiconductor & IC Div.
Nippon Bidg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tel: Tokyo (03) 3270-2111
Fax: (03) 3270-5109

For further information write to:
Hitachi America, Ltd.
Semiconductor & IC Div.
2000 Sierra Point Parkway
Brisbane, CA. 94005-1835
U S A
U S A
Tel: 415-589-8300
Fax: 415-583-4207
Fax: 298-9 29 1 80-0
Fax: 298-9 29 30 0 0 München Tel: 089-9 91 80-0 Fax: 089-9 29 30 00

Hitachi Europe Ltd.
Electronic Components Div.
Northern Europe Headquarters
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA
United Kingdom
Tel: 0628-585000
Fax: 0628-778322

Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 0104 Tel: 535-2100 Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 706, North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: 27359218 Fax: 27306071